

DISCIPLINE – ELECTRICAL ENGG SEMESTER 5TH SUB-PE & PLC LAB No periods Per Week Class Alloted-3	LESSON PLAN NAME OF THE TEACHING FACULTY-NIHARIKA SETHY SEMESTER FROM 14.07.2025 TO 15.11.2025	
WEEK	EXPERIMENT	STATUS
1	Study of switching characteristics of a power transistor	
2	Study of V-I characteristics of SCR	
3	Study of V-I characteristics of TRIAC.	
4	Study of V-I characteristics of DIAC	
5	Study of drive circuit for SCR & TRIAC using DIAC	
6	Study of drive circuit for SCR & TRIAC using UJT.	
7	To study phase controlled bridge rectifier using resistive load	
8	To study series Inverter.	
9	Study of voltage source Inverter	
10	To perform the speed control of DC motor using Chopper	
11	To study single-phase Cyclo-converter	
12	Introduction/Familiarization PLC Trainer & its Installation with PC Learn the basics and hardware components of PLC, Understand configuration of PLC system, Study various building blocks of PLC, Determine the No. of digital I/O & Analog I/O	
13	Execute the different Ladder Diagrams Demonstrate PLC and Ladder diagram-Preparation downloading and running Execute Ladder diagrams for different Logical Gates	
14	Execute Ladder diagrams using timers & counters ,Execute the Ladder Diagrams with model applications	
15	DOL starter (ii)Star- Delta starter Execute Ladder diagrams with model applications (i) Stair case lighting (ii) Traffic light controller	

